

JUL 20 2006

Docket No. 031948-6

Serial No. 10/760,295

Page 1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Kenji ARAI et al.

Serial No. 10/760,295

Filed: 1/21/2004

For: METHOD OF PROTECTING A
SEMICONDUCTOR INTEGRATED
CIRCUIT FROM PLASMA DAMAGE

)

)

)

)

)

)

)

Group Art Unit: 2825

Examiner: Yelena Rossoshek

Confirmation No. 9845

Date: July 20, 2006

CERTIFICATE OF MAILING OR TRANSMISSION
[37 CFR 1.8(a)]

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to: Mail Stop _____, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, or being facsimile transmitted to the USPTO at 571 273 8300 on July 20, 2006.

Signature: _____

Name: Jeannie Saxton

AMENDMENT

Mail Stop _____
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 20, 2006, please amend the above identified application as follows.

07/21/2006 TL0111 00000069 192380 10760295

02 FC:1201 200.00 DA